TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

## 2SK2417

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain-source ON resistance : RDS (ON) = 0.42  $\Omega$  (typ.)

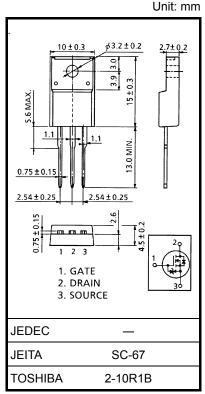
• High forward transfer admittance  $: |Y_{fs}| = 7.5 \text{ S (typ.)}$ 

• Low leakage current  $: IDSS = 100 \mu A \text{ (max) (VDS} = 250 \text{ V)}$ 

• Enhancement mode  $: V_{th} = 1.5 \text{ to } 3.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$ 

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	250	V
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	$V_{DGR}$	250	V
Gate-source voltage		$V_{GSS}$	±20	V
Drain current	DC (Note 1)	I <sub>D</sub>	7.5	Α
	Pulse (Note 1)	I <sub>DP</sub>	30	Α
Drain power dissipation	n (Tc = 25°C)	P <sub>D</sub>	30	W
Single pulse avalanch	e energy (Note 2)	E <sub>AS</sub>	110	mJ
Avalanche current		I <sub>AR</sub>	7.5	Α
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	3	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55~150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	4.16	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 50 V,  $T_{ch}$  = 25°C (initial), L = 3.3 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 7.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

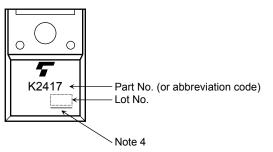
#### **Electrical Characteristics (Ta = 25°C)**

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V		_	±10	μΑ
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	250	_	_	V
Gate threshold v	/oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	_	3.5	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.5 A		0.42	0.5	Ω
Forward transfer	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 3.5 A	4	7.5	_	S
Input capacitano	e	C <sub>iss</sub>			700	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	80	_	
Output capacitance		C <sub>oss</sub>	1		270	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS}$ $V_{OUT}$ $V_{OUT}$ $V_{CS}$ $V_{OUT}$ $V_{CS}$	_	10	_	
	Turn-on time	t <sub>on</sub>		_	20	_	
	Fall time	t <sub>f</sub>		_	10	_	ns
	Turn-off time	t <sub>off</sub>	$V_{DD} = 100V$ Duty $\leq 1\%$ , $t_w = 10 \mu s$	_	70	_	
Total gate charge (Gate-source plus gate-drain)		Qg		-	20	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 200 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7.5 \text{ A}$		13	_	nC -
Gate-drain ("miller") charge		$Q_{gd}$			7	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	7.5	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	30	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 7.5 A, V <sub>GS</sub> = 0 V	_	_	-2.0	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 7.5 A, V <sub>GS</sub> = 0 V	1	180	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> / dt = 100 A / μs		1.1	_	μC

#### Marking

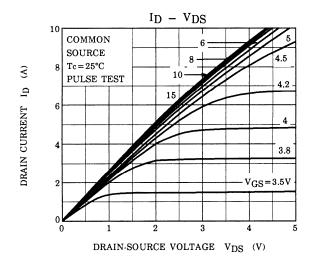


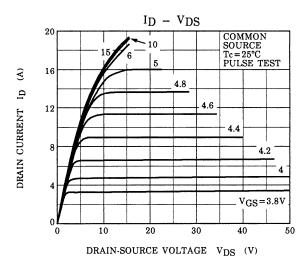
Note 4: A line under a Lot No. identifies the indication of product Labels.

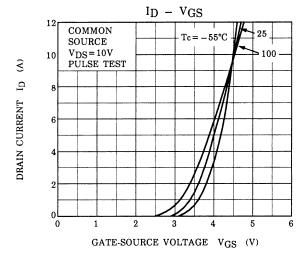
Not underlined: [[Pb]]/INCLUDES > MCV

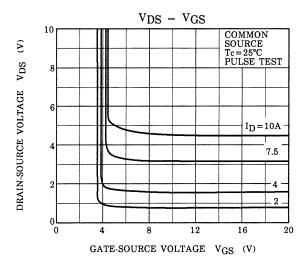
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

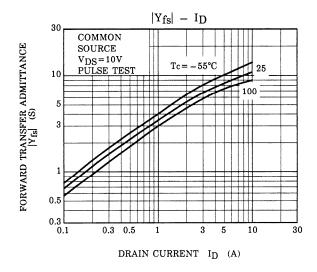
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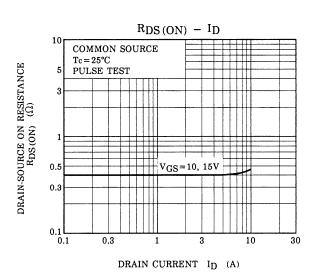


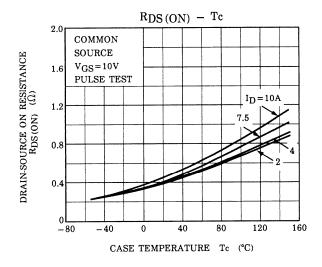


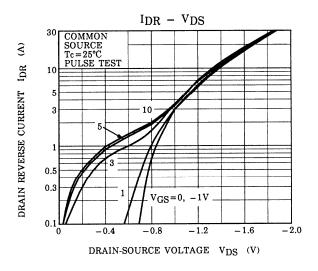


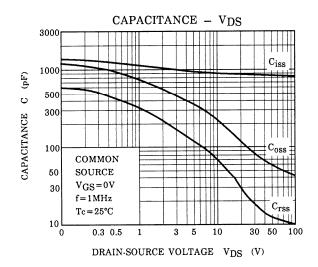


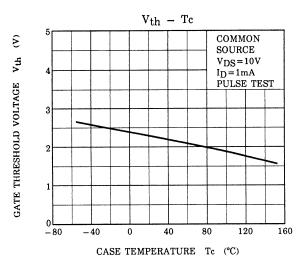


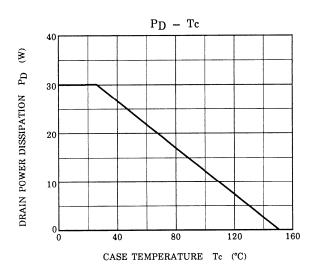


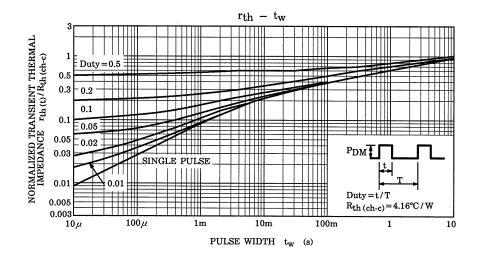


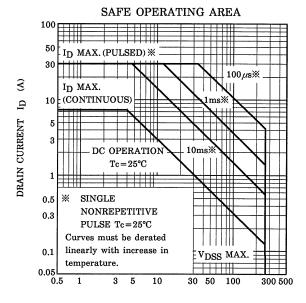


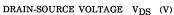


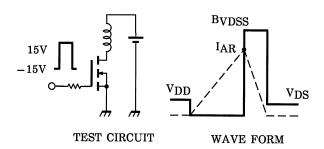












$$\begin{split} R_G &= 25~\Omega \\ V_{DD} &= 50~V,~L = 3.3~mH \end{split}$$

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$$EAS = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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